



Docket: 0756-1653

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of)
Shunpei YAMAZAKI et al.)
Serial No. 08/818,884)
Filed: March 17, 1997)
For: ELECTRO-OPTICAL DEVICE AND)
METHOD FOR MANUFACTURING)
THE SAME)

Art Unit: 251 **CERTIFICATE OF MAILING**

Examiner: T. Nguyen

I hereby certify that this
correspondence is being deposited with
the United States Postal Service with
sufficient postage as First Class Mail in
an envelope addressed to: Assistant
Commissioner for Patents, Washington,
D.C. 20231, on 9-30-98

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:


Transmitted herewith is a Clarification of Information Disclosure
Statement and Submission of English Translation in the above-identified
application.

[X] No additional fee is required.

[X] In the event applicant(s) has overlooked the need for any
petition and fee for extension of time, and such
extension is required, applicant(s) requests that this be
considered a petition therefor and that such fee be
charged to Deposit Account No. 19-2380.

[X] The Commissioner is hereby authorized to charge fees
under 37 CFR 1.16, 1.17, 1.20(a), 1.20(b), 1.20(c) and
1.20 (d) (except the Issue Fee) which may be required now
or hereafter, or credit any overpayment, to Deposit
Account No. 19-2380. A duplicate copy of this sheet is
attached.

Respectfully submitted,


Eric J. Robinson
Reg. No. 38,285

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Docket: 0756-1653

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of)
Shunpei YAMAZAKI et al.)
Serial No. 08/818,884) Art Unit: 2515
Filed: March 17, 1997) Examiner: T. Nguyen
For: ELECTRO-OPTICAL DEVICE AND)
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SAME)

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CLARIFICATION OF INFORMATION DISCLOSURE STATEMENT
AND SUBMISSION OF ENGLISH TRANSLATION

CERTIFICATE OF MAILING

Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 9-30-98
Jose M. Garcia

On August 13, 1998, applicant filed an *Information Disclosure Statement* and PTO 1449 Form in the above-identified application.

Upon review of the references cited, it was noted that the description of Japanese Patent Document 2-251992 was incorrect and the Examiner is asked to please disregard the brief description of Japanese Patent Document 2-251992 cited in the *Information Disclosure Statement* filed August 13, 1998. Rather, submitted herewith is an English translation of Japanese Patent Document 2-251992 which was listed on the PTO 1449 Form for consideration by the Examiner.

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Further, the brief description of Document 2-251992 cited in the *Information Disclosure Statement* filed August 13, 1998 should have been directed to Japanese Patent Document 1-289917. That is, Document 1-289917 teaches to manufacture a peripheral circuit and active matrix circuit elements through the same process. Also, Document 1-289917 is the priority application of U.S. Patent 5,250,931, which was also submitted for consideration by the Examiner.

Respectfully submitted,



Eric J. Robinson

Reg. No. 38,285

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